

AMENDMENTS TO THE SPECIFICATION

With reference to U.S. Patent Application Publication US 2004/0028242 (this application), please replace paragraph [0007] with the following replacement paragraph:

[0007] FIG. 1 is a block diagram schematically showing a configuration of a 1-bit amplifier in the past. In FIG. 1, a ~~$\Theta\Sigma-\Delta\Sigma$~~ modulation portion 52 converts a digital audio 1-bit signal reproduced from a CD 51 based on the $\Delta\Sigma$ modulation so as to obtain a PWM (Pulse Width Modulation) signal. And it supplies the obtained PWM signal to a driver circuit 53. The driver circuit 53 generates a control signal for driving a power amplifier 54 by using the PWM signal supplied from the $\Delta\Sigma$ modulation portion 52.

Please replace paragraph [0033] with the following replacement paragraph.

[0033] FIG. 6-2 is a diagram showing a configuration example of a 1-bit amplifier according to this embodiment implementing an audio reproducing apparatus according to the present invention. As shown in FIG. 2, the 1-bit amplifier according to this embodiment has a DAC portion 1, a driver circuit 2, a power amplifier 3 and an LPF 4, where the driver circuit 2 controls amplification time of the power amplifier 3 based on a PWM signal generated in the DAC portion 1 from a digital audio signal reproduced from a CD 51 and passes an obtained amplification signal through the LPF 4 so as to obtain an analog audio signal.